

Amendments to the Specification:

Please replace the original specification paragraphs [Para 10] and [Para 24] with the following amended paragraph.

5 [Para 10]

The problem of the prior art is that the reference level signal requires a specific amount of time to approach the DC component of the input signal, meaning that before the approach, the sliced signal output by the comparator may not effectively represent the binary values of the input signals.

10 [Para 24]

In order to achieve ~~to the~~ goal, the phase-detecting, level-determining device 340 and the DAC 360 must cooperate with each other to generate an accurate reference level signal Vc3 for the comparing device 320.

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